IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Harold M. Chase, et al. Examiner: Unassigned

Serial No.: To Be Assigned Art Unit: Unassigned

Filed: Herewith Docket: 12675Z

For: METHOD FOR VLSI SYSTEM Dated: September 16, 2003

DEBUG AND TIMING ANALYSIS

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Sir:

Please amend the subject patent application as follows:

CERTIFICATE OF MAILING BY "EXPRESS MAIL"

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I hereby certify that this correspondence is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. §1.10 on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Dated: September 16, 2003

John & Sensny John Sensny